

PATENT NUMBER

<p>O.I.P.E.</p> <p>FA SCANNED <i>pc1</i> Q.A. <i>AA</i></p>	<p>PATENT DATE</p>
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APPLICATION NO. 09/920978	CONT/PRIOR D	CLASS 438	SUBCLASS 424	ART UNIT 2813	EXAMINER B/um
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TITLE	APPLICANTS
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95. Member	95. Mr. J.

Shane Trapp

BLUM

Method of forming integrated circuitry and method of forming shallow trench isolation in a semiconductor substrate

PTO-2040
12/89[illegible]

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